

The Piezoresistive Effect in Top–Down Fabricated p-Type 3C-SiC Nanowires

Hoang-Phuong Phan, Toan Dinh, Takahiro Kozeki, Tuan-Khoa Nguyen, Afzaal Qamar, Takahiro Namazu, Nam-Trung Nguyen, and Dzong Viet Dao

Abstract—This letter reports on the piezoresistive effect of top–down fabricated 3C-SiC nanowires (NWs). Focused ion beam was utilized to create p-type 3C-SiC NWs from a 3C-SiC thin film with a carrier concentration of $5 \times 10^{18} \text{ cm}^{-3}$ epitaxially grown on a Si substrate. The as-fabricated NWs were then subjected to tensile strains varying from 0 to 280 $\mu\epsilon$. Experimental data showed that the p-type 3C-SiC NWs possess a large gauge factor of 35, which is at least one order of magnitude larger than that of other hard materials, such as carbon nanotubes and graphene. This large gauge factor and the linear relationship between the relative resistance change and the applied strain in the SiC NWs indicate their potential for nanoelectromechanical systems sensing applications.

Index Terms—Silicon carbide, piezoresistive effect, nanowires, NEMS sensors, focused ion beam.

I. INTRODUCTION

PIEZORESISTIVE effect is one of the most commonly used sensing mechanisms in Micro Electro Mechanical Systems (MEMS) transducers, owing to its simple readout and compatibility with integrated circuits [1]–[6]. The piezoresistive effect in nano structures has attracted great attention as a means of device miniaturization [7]–[9]. For instance, a large number of studies have been carried out on silicon nanowires (SiNWs) – the most popular semiconductors used in MEMS. Applications of Si NW based piezoresistive sensors can be found in pressure sensors [10], flow sensors [12], accelerometers [11], and biomass detecting sensors [13].

Demands for electronics which can operate at high frequencies and high corrosion conditions have prompted the research on high stiffness materials to replace Si based counterparts. Thanks to their large Young’s modulus (above 300 GPa), carbon based devices can offer higher resonance frequencies and Q-factors as compared to Si based devices [14]. The excellent mechanical properties also make carbon based materials suitable for extensive abrasion, such as the scanning tips of AFM (Atomic force Microscopy) based data storage

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H.-P. Phan, T. Dinh, T.-K. Nguyen, A. Qamar, N.-T. Nguyen, and D. V. Dao are with the Queensland Micro- and Nanotechnology Centre, Griffith University, Nathan QLD 4111, Australia (e-mail: hoangphuong.phan@griffithuni.edu.au).

T. Kozeki is with the Department of Mechanical Engineering, University of Hyogo, Himeji 671-2280, Japan.

T. Namazu is with the Department of Mechanical Engineering, Aichi Institute of Technology, Toyota 470-0392, Japan.

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systems [15]. The piezoresistive effect in CNTs [16], [17], graphene [18]–[20], and CNT yarns [21], [22] has also been investigated to develop niche sensing applications. The gauge factor of pure CNTs, CNT yarns, and graphene has been reported to be in the range of 0 to 10. To enhance the gauge factor of these materials, sophisticated structures such as CNT cross-links [23], graphene networks [24], [25], and back-gated CNT FETs [26] are required. More importantly, to integrate these materials with CMOS (Complementary Metal-Oxide Semiconductor) devices, additional processes such as exfoliation, sampling, and/or transferring are required [16], [20]. Therefore a wafer-scale high stiffness material which can be directly integrated with CMOS devices, and can offer a significant piezoresistive effect is preferable for ubiquitous mechanical sensors.

Single crystalline cubic silicon carbide (3C-SiC), a carbon based material, can solve the above-mentioned bottleneck, as it can be grown on a large scale Si substrate [27], [28]. This capability advances the fabrication of SiC based on the conventional MEMS process, and also makes 3C-SiC compatible with CMOS devices. The large energy band gap, and excellent physical properties have proved the feasibility of 3C-SiC in MEMS transducers such as strain, temperature, and optical sensors operating in harsh environments [29]–[31]. The piezoresistive effect in SiC NWs has also been reported recently [32]–[34]. However, most of these studies focused on bottom-up SiC NWs, while the piezoresistive effect of top-down fabricated SiC has not been fully understood. In addition, in the previous work on the piezoresistive effect in SiC NWs, external mechanical strains were locally induced into a certain point of SiC NWs [33], [34], while the effect of the uniaxial strain on the conductivity of SiC NWs has been rarely reported.

This letter presents the characterization of the piezoresistive effect of p-type 3C-SiC NWs, fabricated using a top-down process. The gauge factor of the SiC NWs under uniaxial strain was found to be 35. Taking the advantages of the compatibility with conventional MEMS processes and the capability of integrating with other CMOS devices, the as-fabricated SiC NWs on a Si substrate show their significant potential for NEMS mechanical sensors such as piezoresistive AFM, and self-sensing nano resonators.

II. SAMPLE PREPARATION

Figure 1(a) shows the fabrication of the SiC NWs used in this study. Prior to the fabrication of SiC NWs, a 3C-SiC thin film with a thickness of 300 nm was grown on a Si wafer with a diameter of 300 mm using low pressure

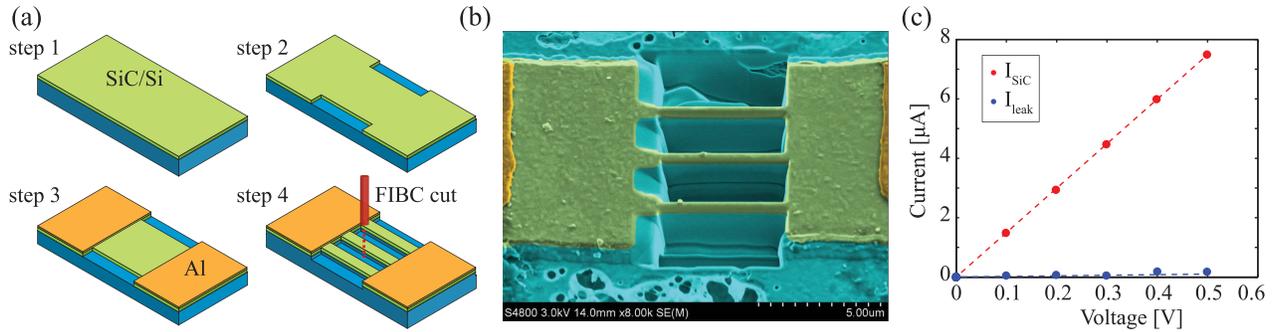


Fig. 1. (a) Fabrication of SiC NWs using a photolithography process and Focused Ion Beam; (b) SEM image of the as-fabricated SiC NWs in which the thickness, width, and length of the NWs were 300 nm, 300 nm and 5 μm , respectively. The SiC NWs were aligned along [110] direction; (c) The current-voltage of a SiC NWs array and the current leakage through the SiC/Si junction.

chemical vapor deposition. The details of the growth process can be found elsewhere [35], in which SiH_4 and C_3H_6 were employed as precursors. P-type 3C-SiC was formed where trimethylaluminum (TMAI) was the dopant. The properties of the SiC film has been reported in our previous studies [36]. The crystalline quality was characterized using high resolution transmission electro microscopy, indicating that crystal defect mainly distributed at the vicinity of the SiC/Si interface. As the thickness of the SiC film was 300 nm, which is much larger than the thickness of the high density defect layer, the influence of the defect layer on the electrical conductivity of SiC is insignificant [36]. Following the growth process, a thin layer of Al was deposited and patterned to create electrode pads. SiC micro patterns were then etched using Inductively Coupled Plasma. Next, SiC on Si strips with their lengths, widths and thicknesses of 60 mm, 8 mm, and 625 μm respectively, were diced from the SiC/Si wafer for the subsequent bending experiment. Finally, SiC NWs were formed using FIB (HITACHI FB 2200 TM), where gallium ion beam (Ga^+) was used to bombard the target material [37]. The applied voltage and DC current of the FIB process were 40 kV and 0.07 nA, respectively. Figure 1(b) illustrates the SEM image of a 3C-SiC NWs array. The dimensions of each SiC NW are 300 nm \times 300 nm \times 5 μm .

III. RESULTS

The electrical properties of the SiC film were characterized using a hot probe technique [38]. The polarization of the output voltage in the hot probe measurement indicated that the grown film was p-type SiC. The carrier concentration in the p-type 3C-SiC film was found to be in a range of $5 \times 10^{18} \text{ cm}^{-3}$. The current-voltage characteristic of the as-fabricated SiC NWs was investigated using an Agilent 2722A TM power supply. The linear relationship between the applied voltage and measured current indicates that a good Ohmic contact was formed between the Al electrodes and the SiC NWs resistors, Fig. 1(c). Additionally, utilizing the four-points measurement method, the contact resistance was found to be below 100 Ω , which is negligible compared to the resistance of the SiC NWs. Additionally, the leakage current from the function layer (SiC) to the substrate (Si) was also explored, showing a small leakage of 20 nA at an applied voltage of 0.5 V. The current leakage was much smaller than

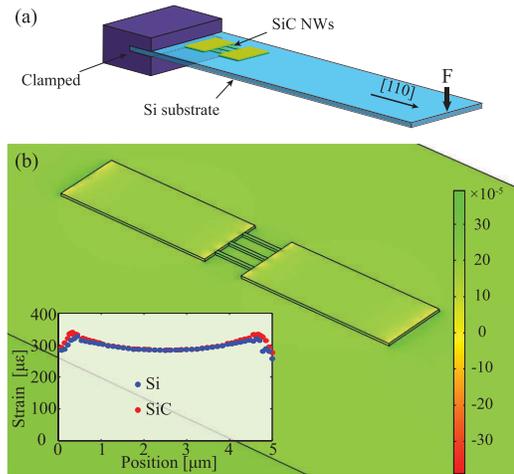


Fig. 2. (a) Schematic sketch of the bending experiment. The distance of from the SiC NWs to the free end of the Si cantilever is 48 mm, which is significantly longer than the length of the SiC NWs (5 μm). (b) Finite element analysis (FEA) results of the strain induced into SiC NWs using the bending beam method. Inset: A comparison between the strain induced into the SiC NWs and that of the top surface of the Si substrate.

the current flowing in the SiC NWs due to the discontinuity of the energy band in SiC and Si. The large energy difference between the top valance bands of SiC ($E_v = 6.9 \text{ eV}$) and Si ($E_v = 5.2 \text{ eV}$) causes a large potential barrier of $\Delta E_v = 1.7 \text{ eV}$ at the SiC/Si heterojunction. This potential barrier prevents holes from flowing across the junction. This result indicates that the low-doped Si substrate ($N_a = 10^{14} \text{ cm}^{-3}$) did not contribute to the measured piezoresistance of the SiC NWs.

Figure 2(a) shows the schematic of the bending beam method which was applied to induce strains into the as-fabricated SiC NWs. In this experiment, the above-mentioned SiC/Si strip was fixed in one side using a metal clamp. The other free side was deflected downward by applying different loads. As the distance from the SiC NWs to the free end of the beam was 48 mm, which is much longer than the length of the SiC NWs, the external strain was almost uniformly induced into the SiC NWs. Numerical calculation of the applied strain was then carried out, using the bilayer model which has been widely employed in piezoelectric actuators [39]. Accordingly, because the thickness of the top layer (300 nm) was much smaller than that of the bottom layer (625 μm Si), the external

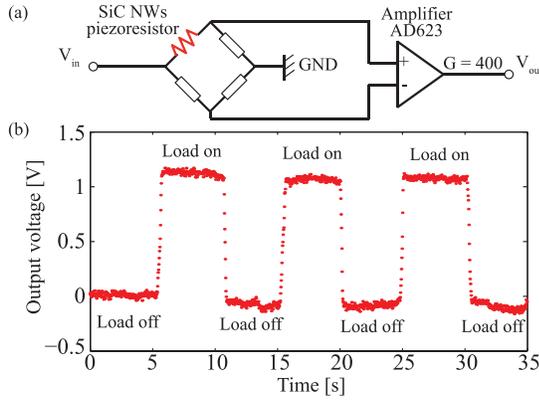


Fig. 3. (a) The Wheatstone bridge and amplifier circuit; (b) The output voltage of a SiC NWs array under an applied tensile strain of $280 \mu\epsilon$ for three bending cycles.

strain was estimated to be approximately 100% transmitted from the top surface of the Si substrate to the SiC NWs [40]. Therefore, the strain induced into the SiC NWs can be calculated using the equation: $\epsilon = 6Fl/(Ewt^2)$, where F is the applied force to the free end of the Si beam, while E , l , w , and t are Young's modulus, the length, the width, and the thickness of the Si beam, respectively. The strains applied to the SiC NWs were also estimated using finite element analysis, in which the Young's moduli of SiC and Si were 330 GPa and 169 GPa, respectively. The simulation results employing COMSOL Multiphysics™ was in solid agreement with the numerical calculation, indicating that the strain of SiC NWs was almost the same as that of the Si substrate, Fig. 2(b). In addition, the strain was slightly higher at the connecting area between the nanowires and electrode pads. This result is considered to be caused by the difference between the width of the electrode pads and that of the SiC NWs. However, this high strain only locally distributed at the connecting area; therefore it did not significantly change the average strain induced into the SiC NWs. Consequently, for applied loads varying from 0 to 0.4 N, the induced strains were found to be in the range of 0 to $280 \mu\epsilon$.

To measure the piezoresistive effect, the resistance change of the SiC NWs was converted into a voltage signal using a Wheatstone bridge and an op-amplifier (Analog Devices™ AD623), Fig. 3(a). The relationship between the relative resistance change ($\Delta R/R$) and the measured output voltage (V_o) is :

$$V_o = \frac{V_i}{4} \frac{\Delta R}{R} G \quad (1)$$

where $V_i = 1 \text{ V}$ is the supplied voltage of the Wheatstone bridge, and $G = 400$ is the gain of the op-amplifier. The output voltages were measured and recorded using an Oscilloscope during the bending experiment. Figure 3(b) shows the output voltage of the SiC NWs at an applied strain of $280 \mu\epsilon$. The repeatability of the output voltage against an external mechanical strain was also confirmed after several bending cycles. The inset in Fig. 4 presents the output voltages of the bridge at different applied strains. Evidently, the output voltage increased with increasing mechanical strains. Subsequently, the relative resistance change ($\Delta R/R$) was calculated

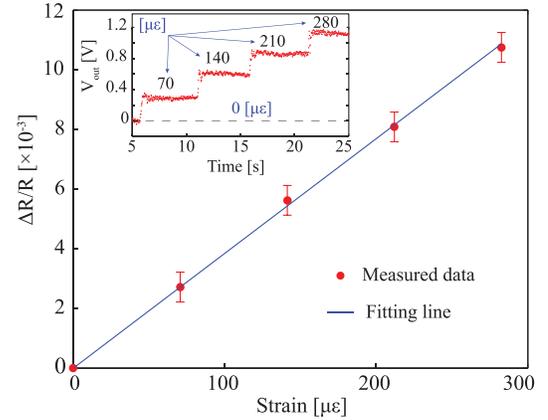


Fig. 4. The linear relationship between the relative resistance change ($\Delta R/R$) of a SiC NWs array against tensile strain varying from 0 to $280 \mu\epsilon$. Inset: the incrementation of the output voltage (V_{out}) with increasing applied strains.

using Eq. 1 and plotted against the applied strains, Fig. 4. Evidently, the relative resistance change has a linear relationship with the applied strains. Consequently, the gauge factor ($GF = (\Delta R/R)/\epsilon$) of the SiC NWs was found to be 35.

The gauge factor found in the SiC NWs was much larger than that of other hard materials such as pure CNTs, and graphene. The piezoresistive effect in p-type 3C-SiC NWs can be qualitatively explained using the valance band modification under strain [33]. As such, an external mechanical strain causing the shift and warping in the heavy hole and light hole bands. As a result, the major carriers (holes) will redistribute in these two bands, following the rule that hole will fill up the lower energy first. Consequently, the redistribution of the holes will change the hole effective mass, as well as their mobility, leading to a change in the resistance of SiC NWs. On the other hand, the piezoresistive effect of pure CNTs and graphene is similar to metal, being mainly caused by the change in geometrical dimensions [20]. The gauge factor of our top-down fabricated NWs is also comparable to that of bottom up NWs characterized using a local point-strain [33], [34]. In addition, as the common strain gauges aim at measuring uniaxial strains, our finding in the piezoresistance of SiC NWs under uniaxial strain is significant for designing SiC NWs based strain sensors.

IV. CONCLUSION

In conclusion, this work reports on the piezoresistive effect in top-down fabricated SiC NWs for mechanical sensing applications. The compatibility with the MEMS photolithography process of the SiC NWs fabricated from SiC on Si wafer demonstrates its potential for mass production as well as suitability for the integration with CMOS devices. Additionally, the significant piezoresistive effect with a gauge factor of 35 found in the p-type 3C-SiC NWs also proved the feasibility of using this new platform for NEMS sensors.

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